

PATENT APPLICATION

ATTORNEY DOCKET NO. CSCO-010/4380

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED  
CENTRAL EXAMINING  
JAN 11 2005

Inventor(s): Amit S. Phadnis, Pankaj Vyas and Balaji Lakshmikanth Bangolae

Serial No.: 09/976,004

Examiner: OLMS, DOUGLAS W

Filing Date: 10/15/2001

Group Art Unit: 2661

Title: Reducing Overhead When Setting Up Multiple Virtual Circuits Using Signaling Protocols

Honorable Commissioner for Patents  
Alexandria, Virginia 22313-1450

STATUS INQUIRY

Sir:

1. More than 38 months have passed since:

☒ NEW APPLICATIONS

the filing of this application on 10/15/2001. No communication has been received from Patent and Trademark Office indicating action on this application.

☐ AMENDED APPLICATIONS

the filing of a response on \_\_\_\_\_. No further communication has been received from the Patent and Trademark Office.

☐ APPEALED APPLICATION

The Appeal Brief was filed on \_\_\_\_\_.

☐ An Examiner's Answer was mailed on \_\_\_\_\_.

☐ A Reply Brief to the Examiner's Answer was submitted on \_\_\_\_\_.

☐ ALLOWED APPLICATIONS

the mailing of Form POL-327 and/or Examiner's Amendment on \_\_\_\_\_.

2. Kindly advise the undersigned of the present status of this application, by checking the appropriate box on the next page.

Respectfully submitted,

By Naren Thappeta

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**PATENT APPLICATION**

**ATTORNEY DOCKET NO. CSCO-010/4390**

**STATUS INQUIRY REPLY**

Please include expected date of first office action

**BY FACSIMILE TO: (707) 356-4172**

APPLICATION SERIAL NO. 09/976,004 is currently

☐ Assigned to Group \_\_\_\_\_ and awaits:

☐ Action by the examiner

☐ Applicant's response to Office Action mailed \_\_\_\_\_.

APPEAL NO. \_\_\_\_\_

☐ Awaiting action by the board of Appeals and Interferences

☐ Date of hearing expected \_\_\_\_\_

☐ Decision expected \_\_\_\_\_.